PAGE 1/6

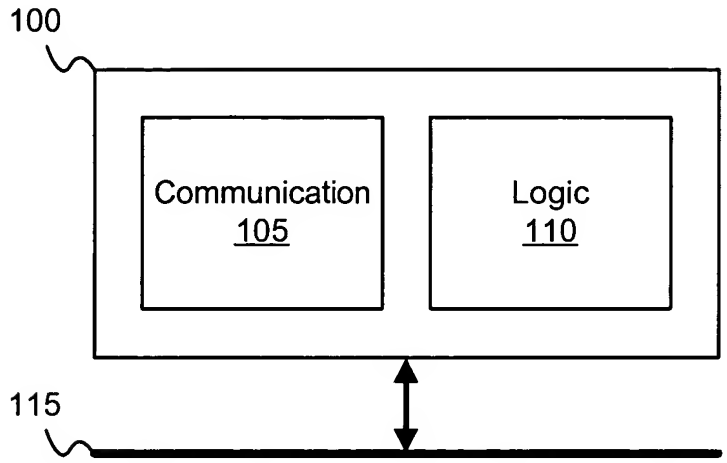

FIG. 1

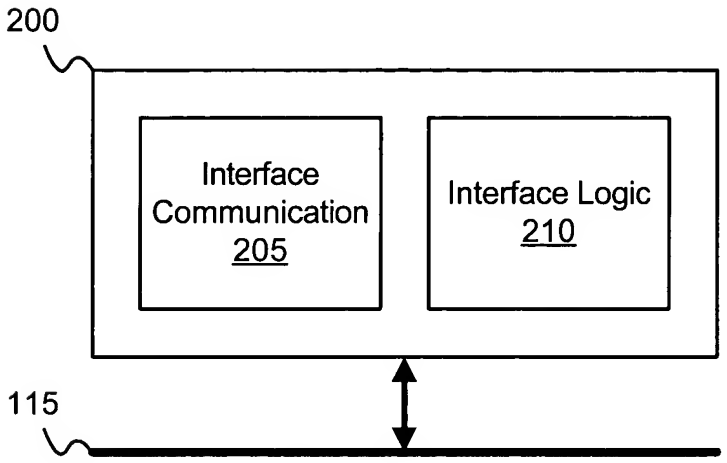

FIG. 2

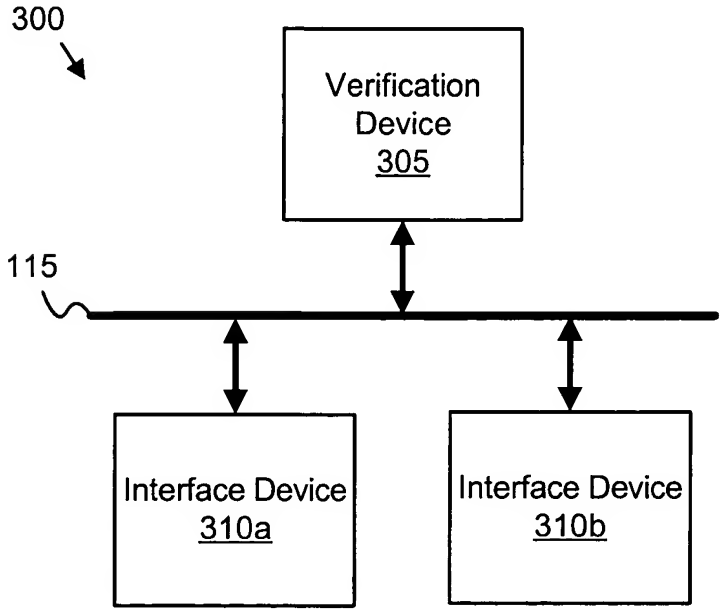
300
Verification Device 305
115
Interface Device 310a
Interface Device 310b

FIG. 3

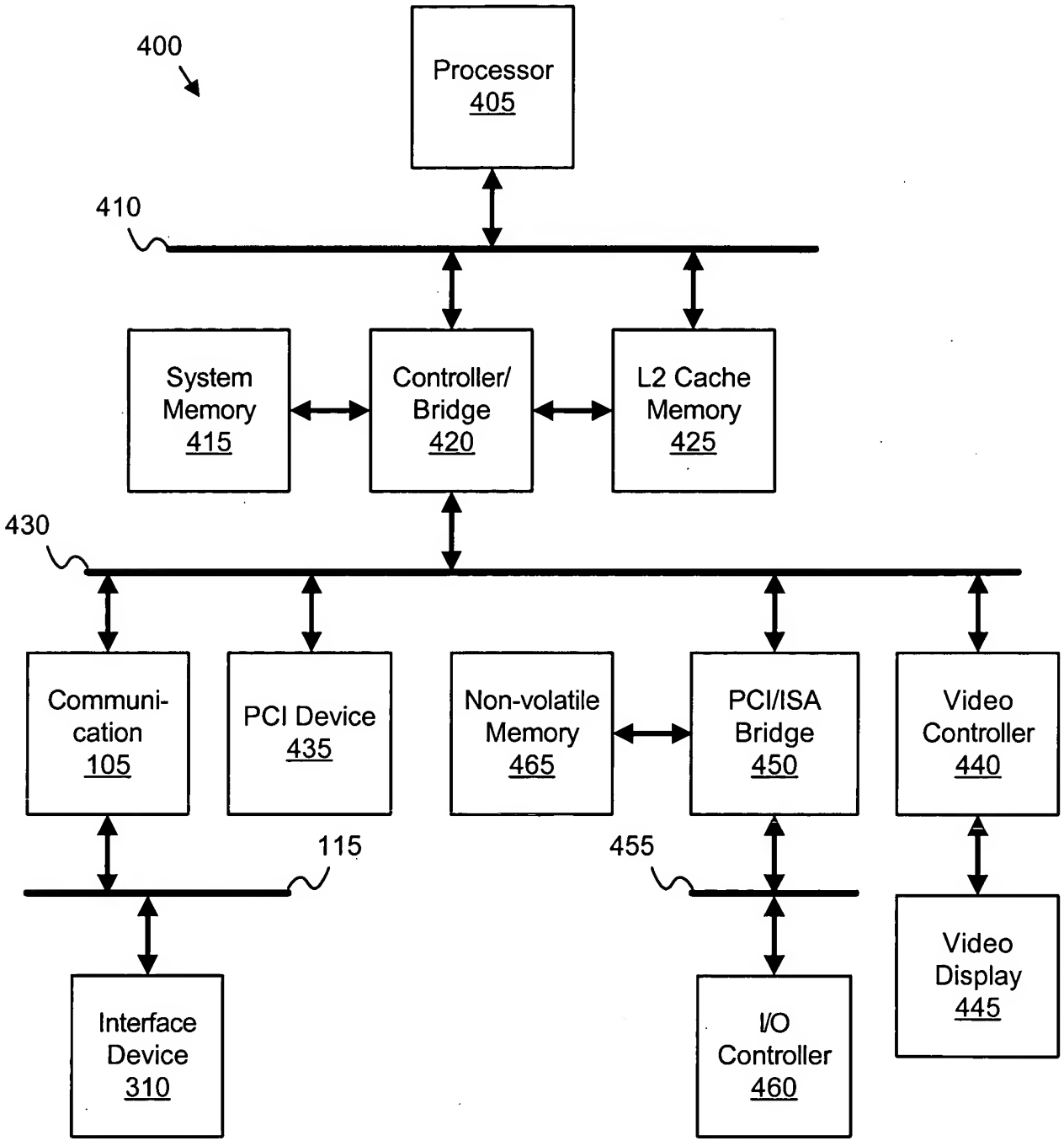
400
Processor
405
410
System
Memory
415
Controller/
Bridge
420
L2 Cache
Memory
425
430
Communi-
cation
105
PCI Device
435
Non-volatile
Memory
465
PCI/ISA
Bridge
450
Video
Controller
440
115
455
Interface
Device
310
I/O
Controller
460
Video
Display
445

FIG. 4

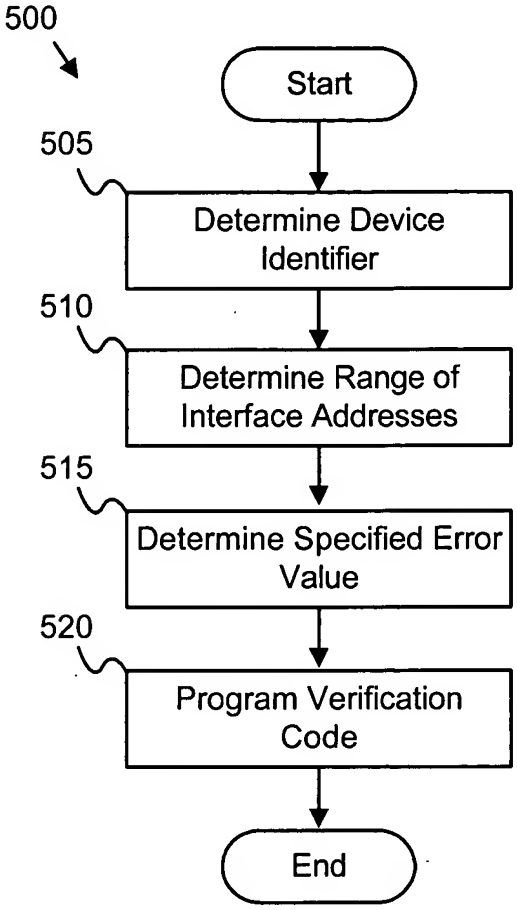
500
Start
505
Determine Device Identifier
510
Determine Range of Interface Addresses
515
Determine Specified Error Value
520
Program Verification Code
End

FIG. 5

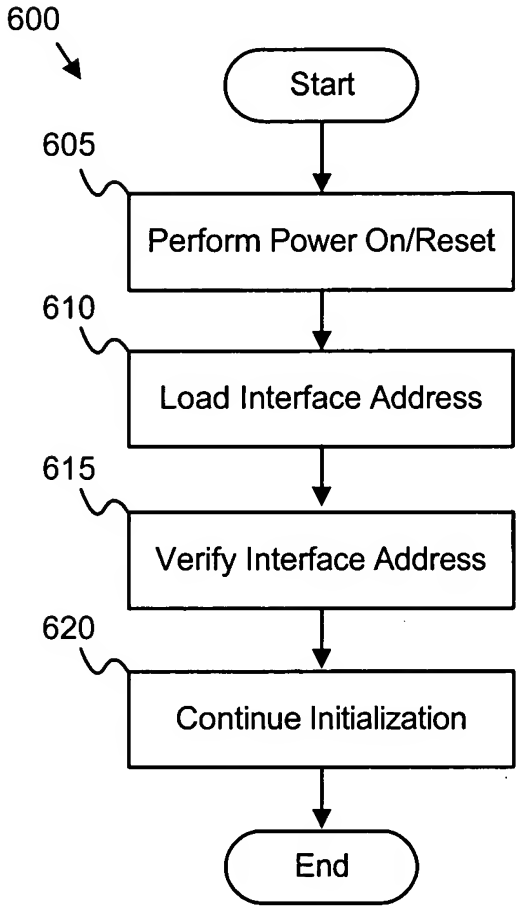
600
Start
605
Perform Power On/Reset
610
Load Interface Address
615
Verify Interface Address
620
Continue Initialization
End

FIG. 6

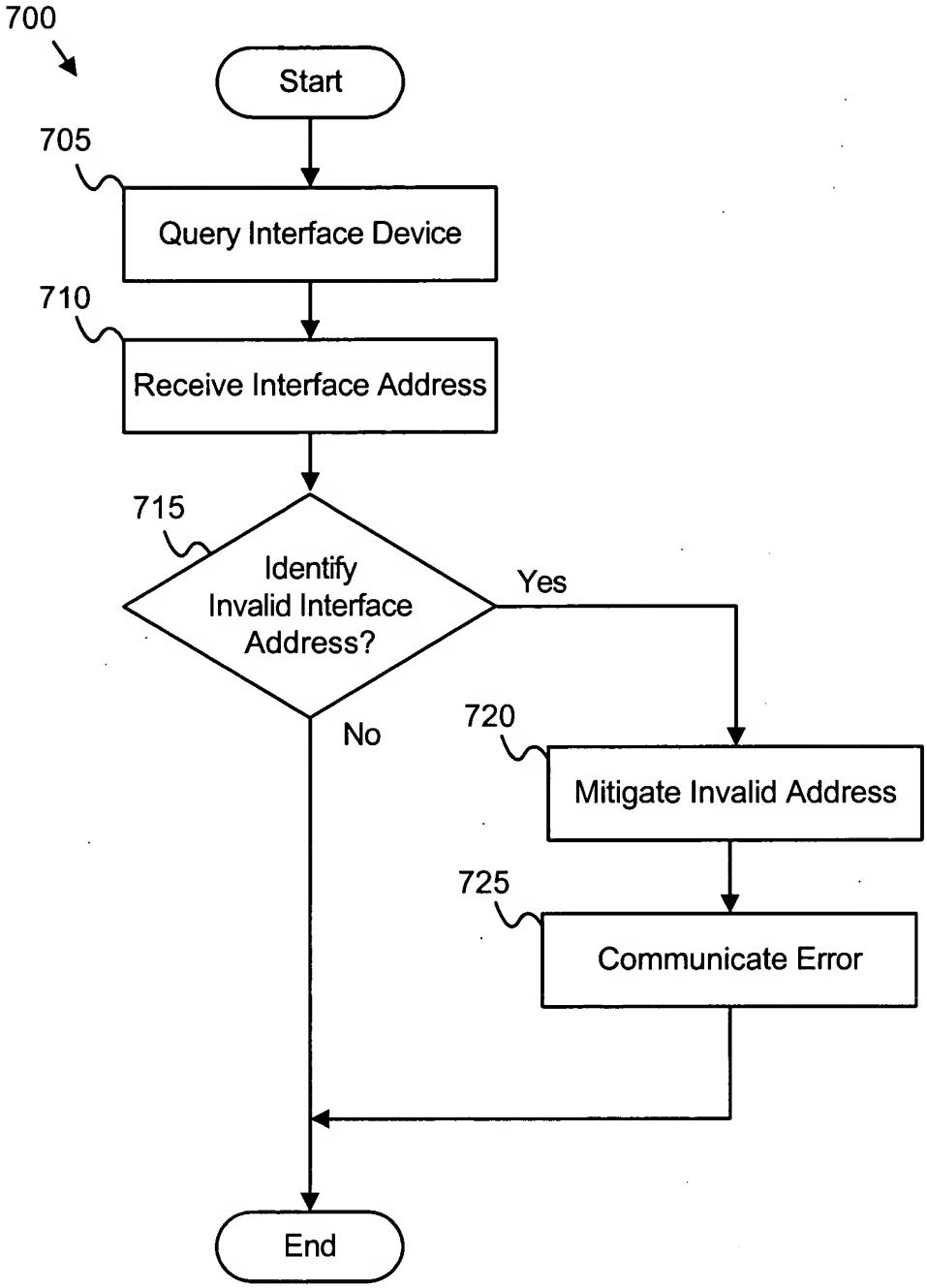
700
Start
705
Query Interface Device
710
Receive Interface Address
715
Identify Invalid Interface Address?
Yes
No
720
Mitigate Invalid Address
725
Communicate Error
End

FIG. 7